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FIG 1

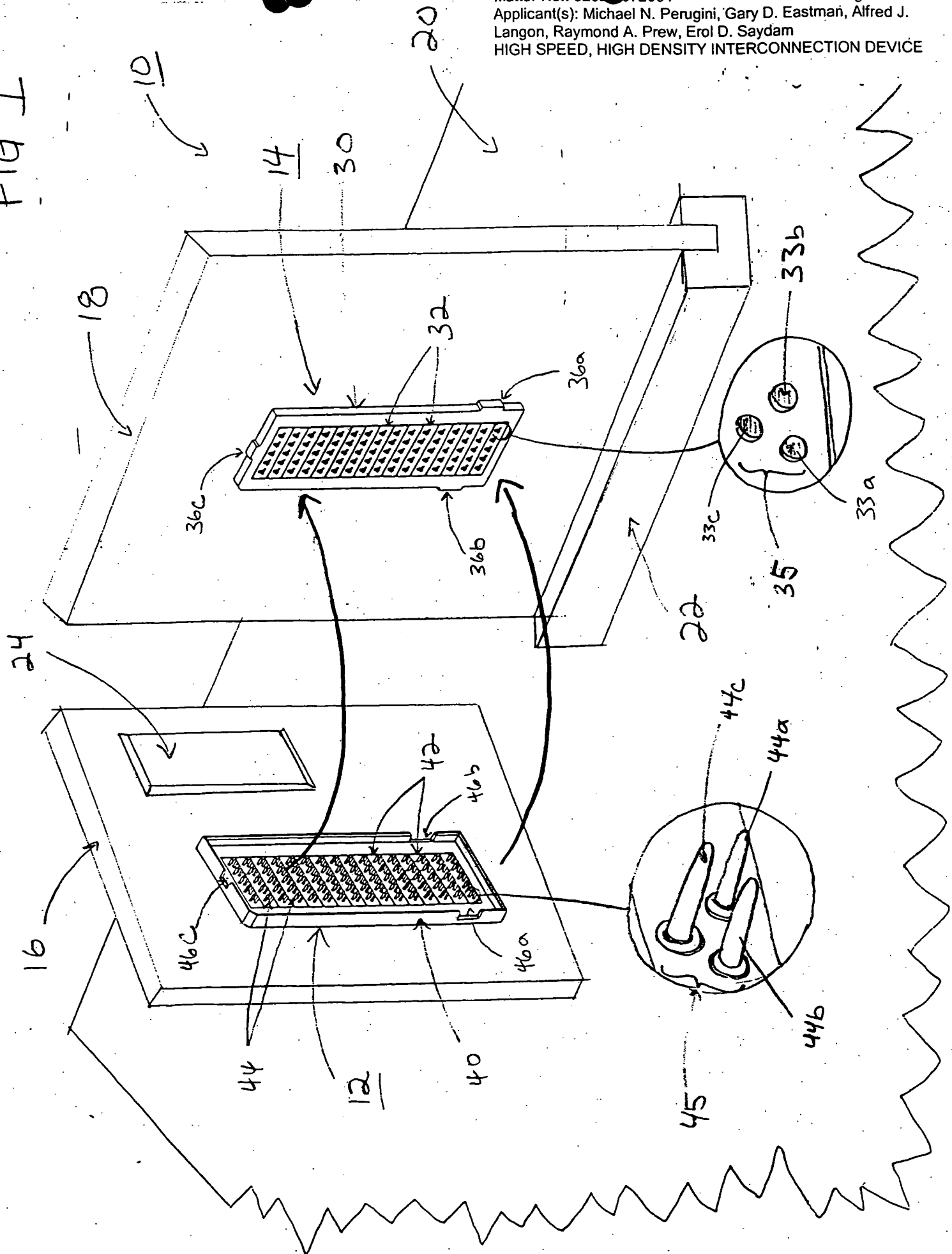


FIG 2A

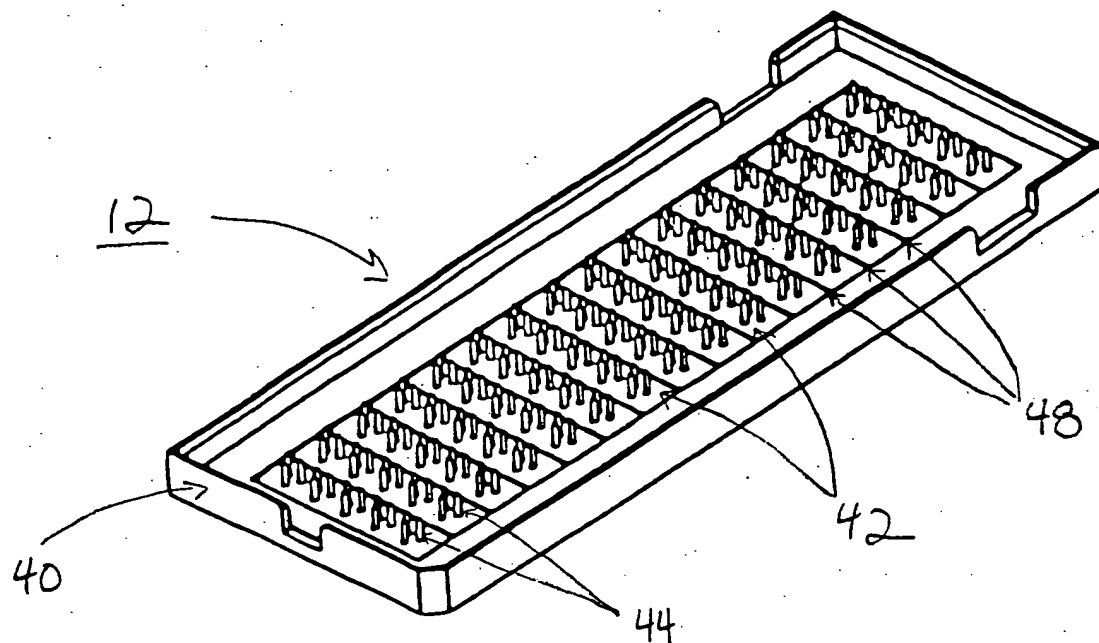


FIG 2B

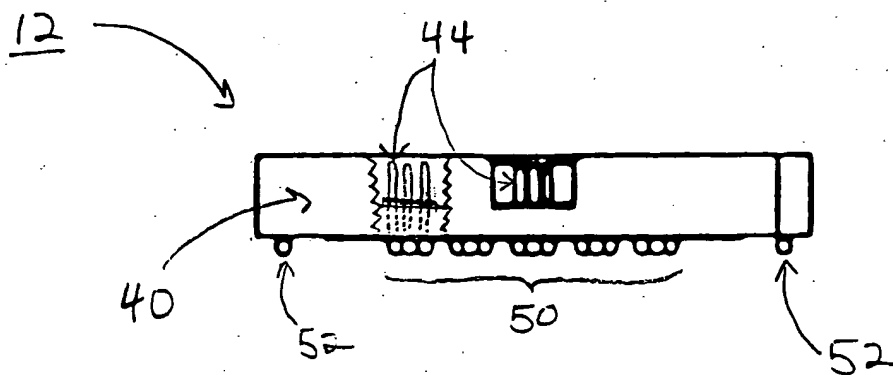


FIG 3A

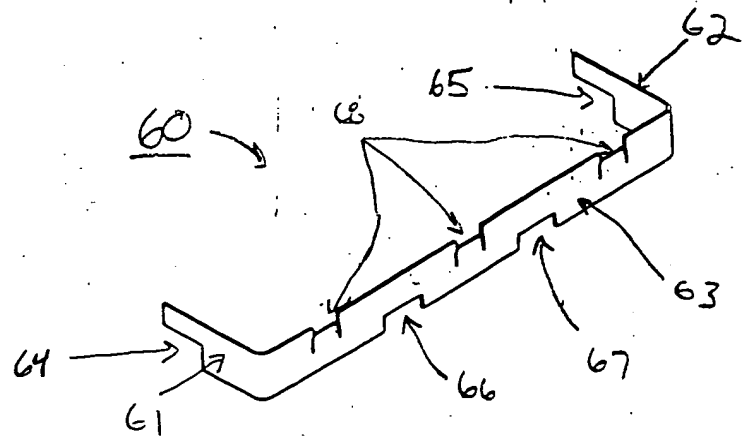


FIG 3B

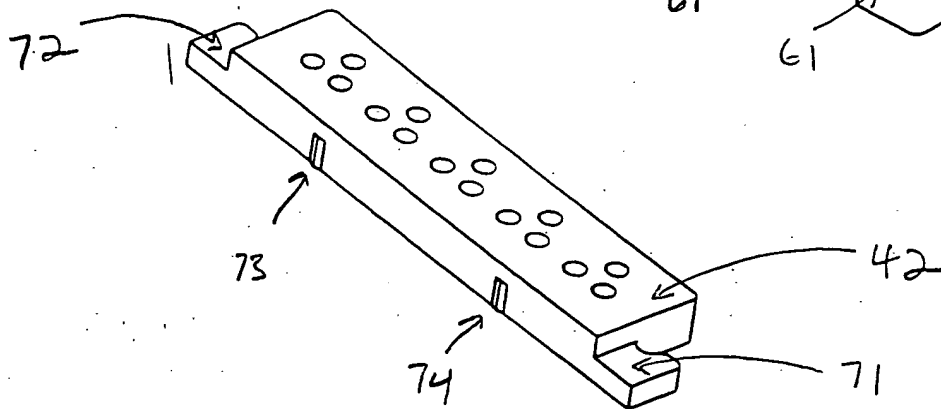
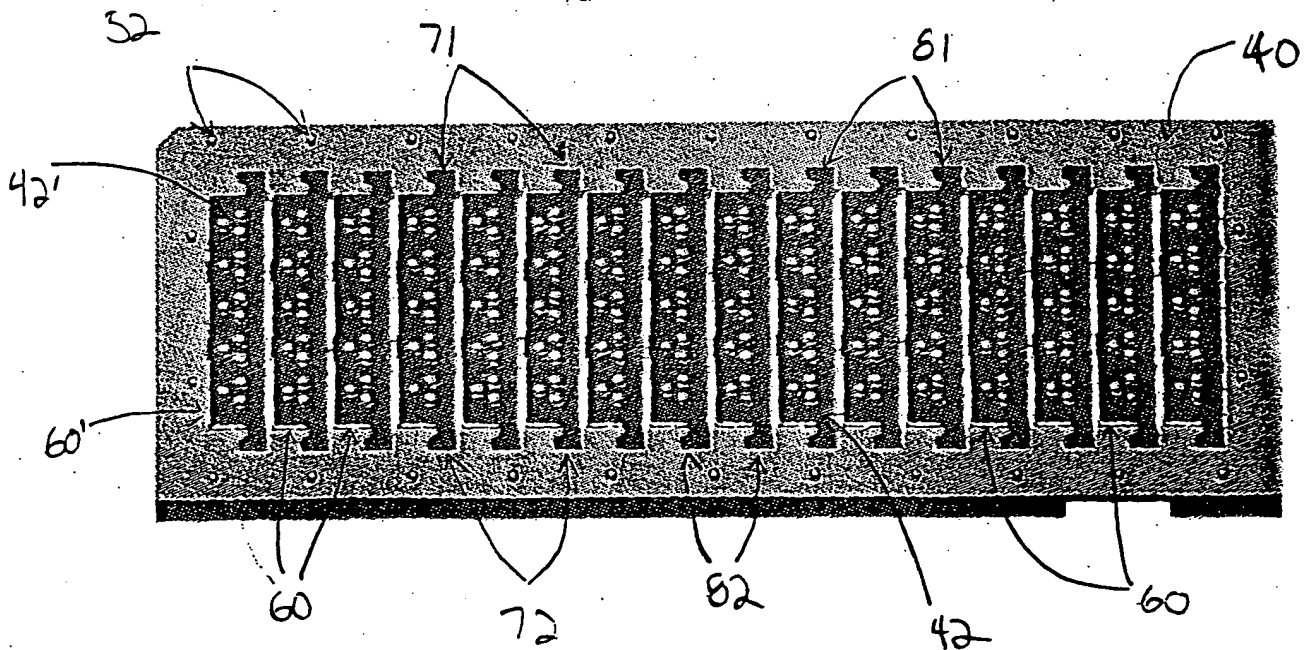


FIG 3C



Applicant(s): Michael N. Perugini, Gary D. Eastman, Alfred J. Langon, Raymond A. Prew, Erol D. Saydam  
HIGH SPEED, HIGH DENSITY INTERCONNECTION DEVICE

FIG 4A

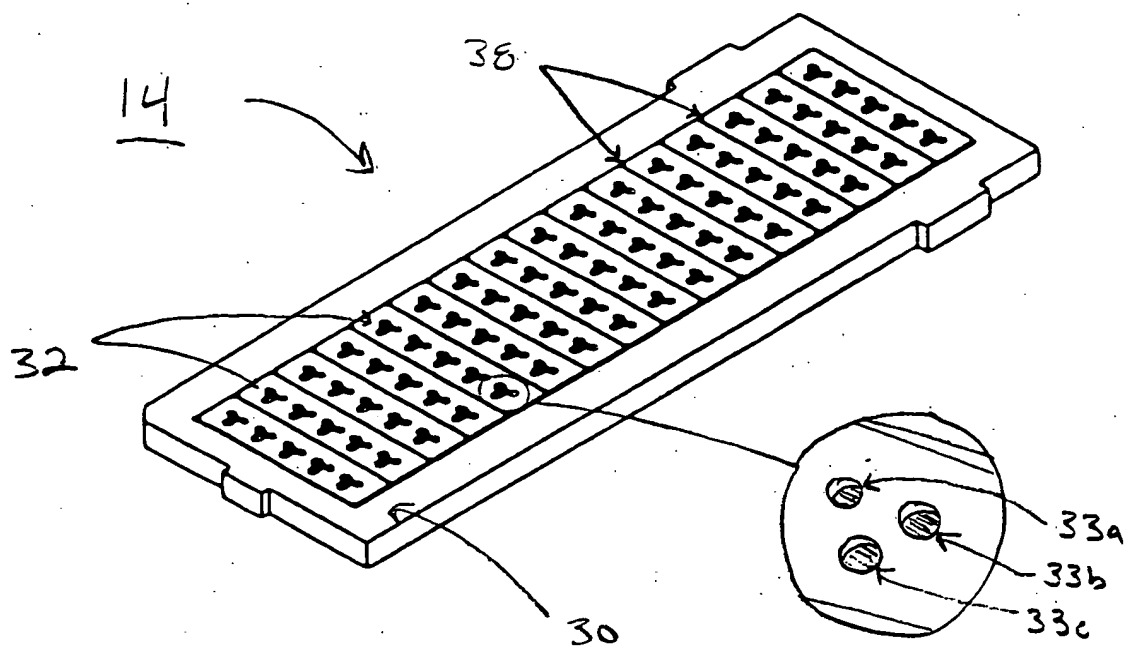


FIG 4B

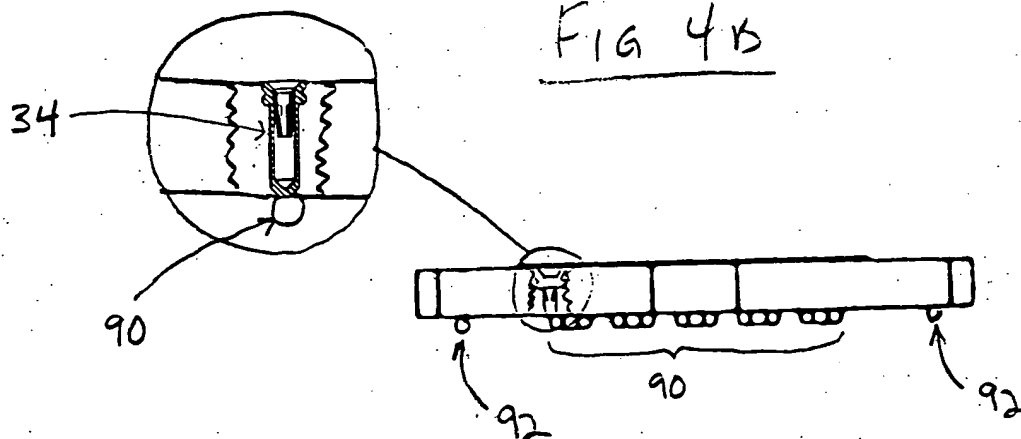


FIG 5A

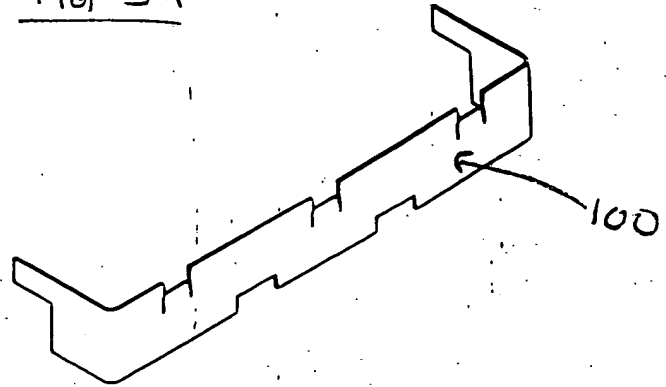


FIG 5B

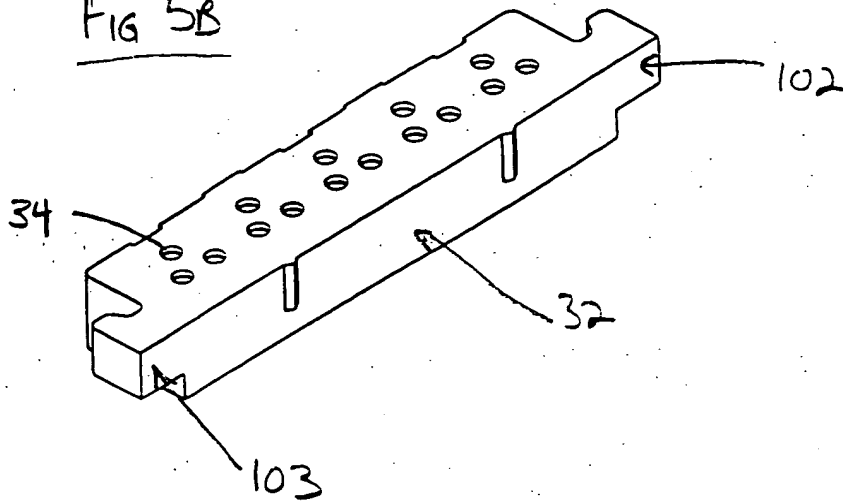


FIG 5C

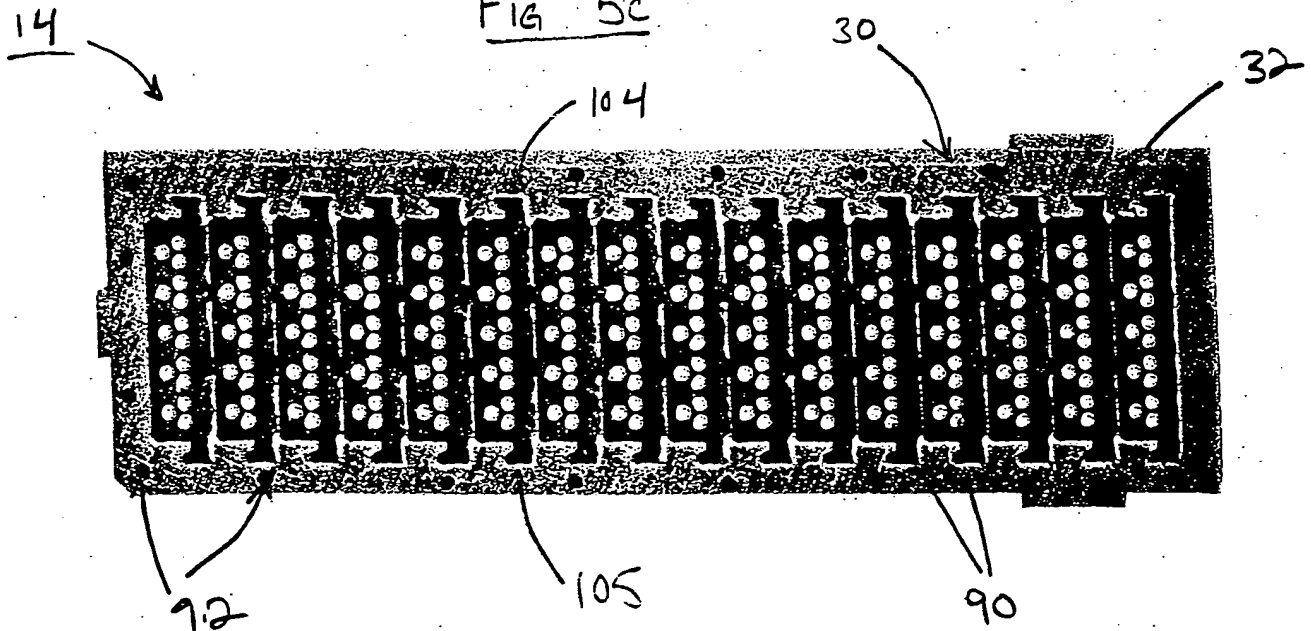


FIG. 6

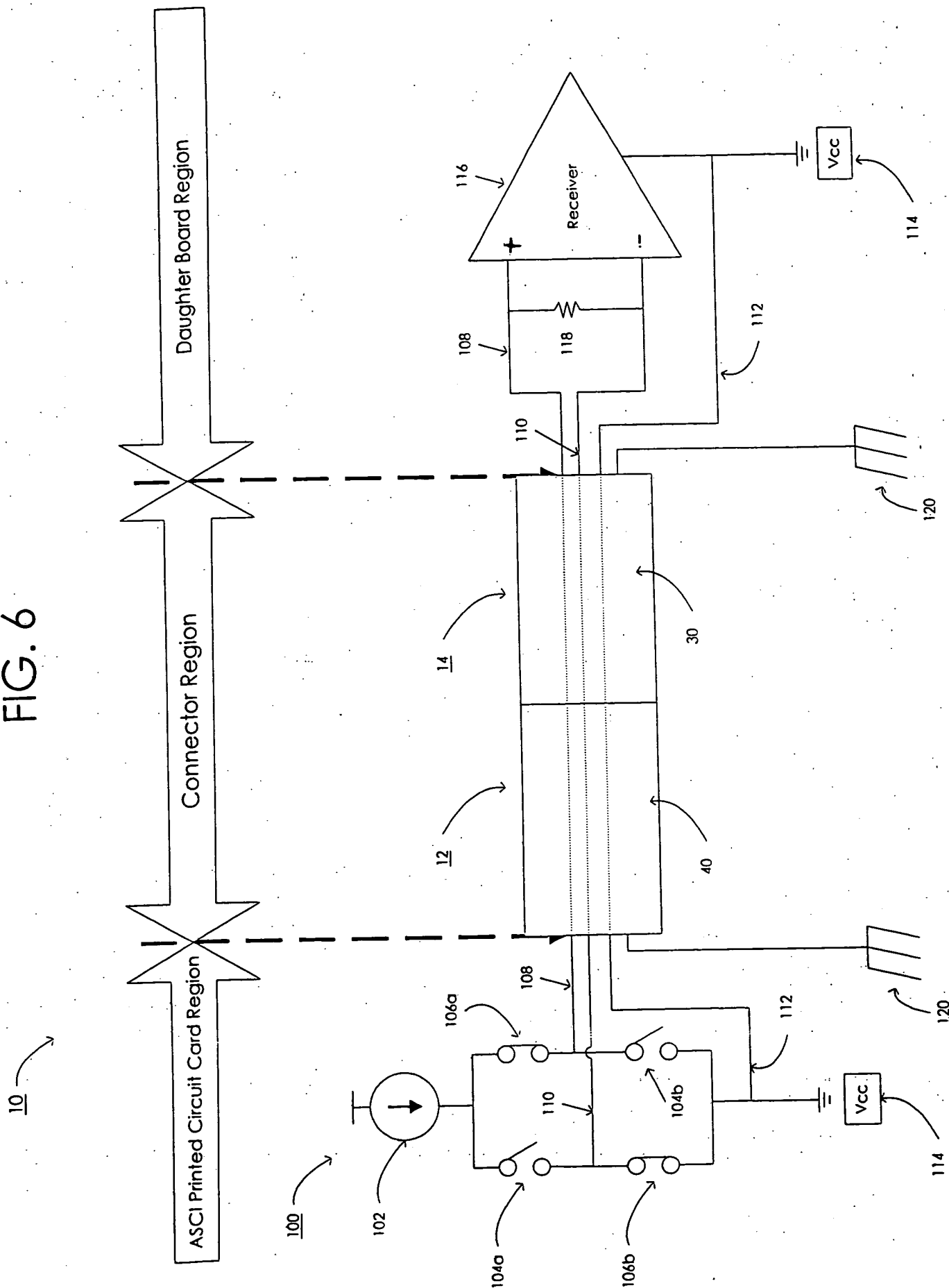


FIG. 7

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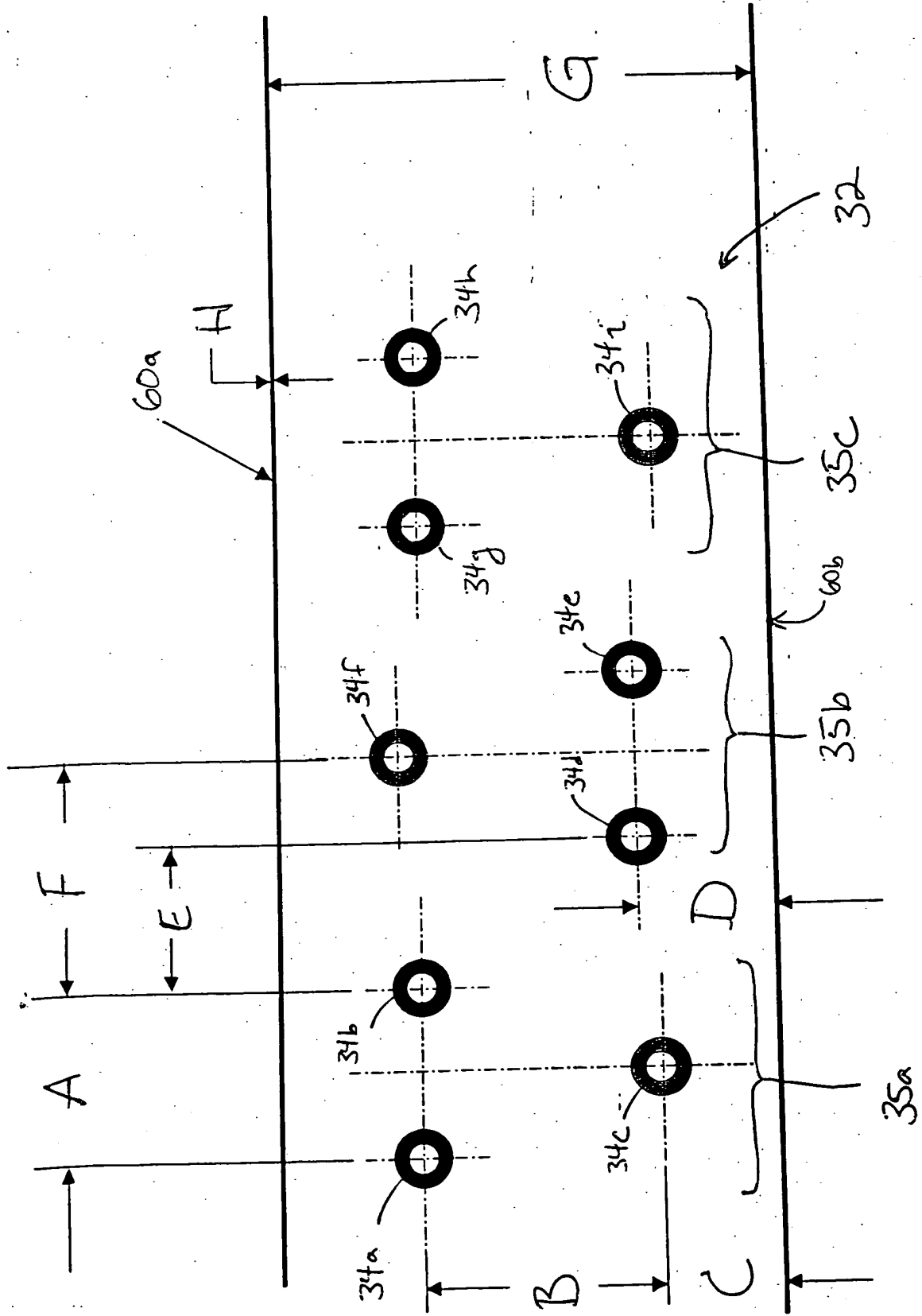




FIG. 8

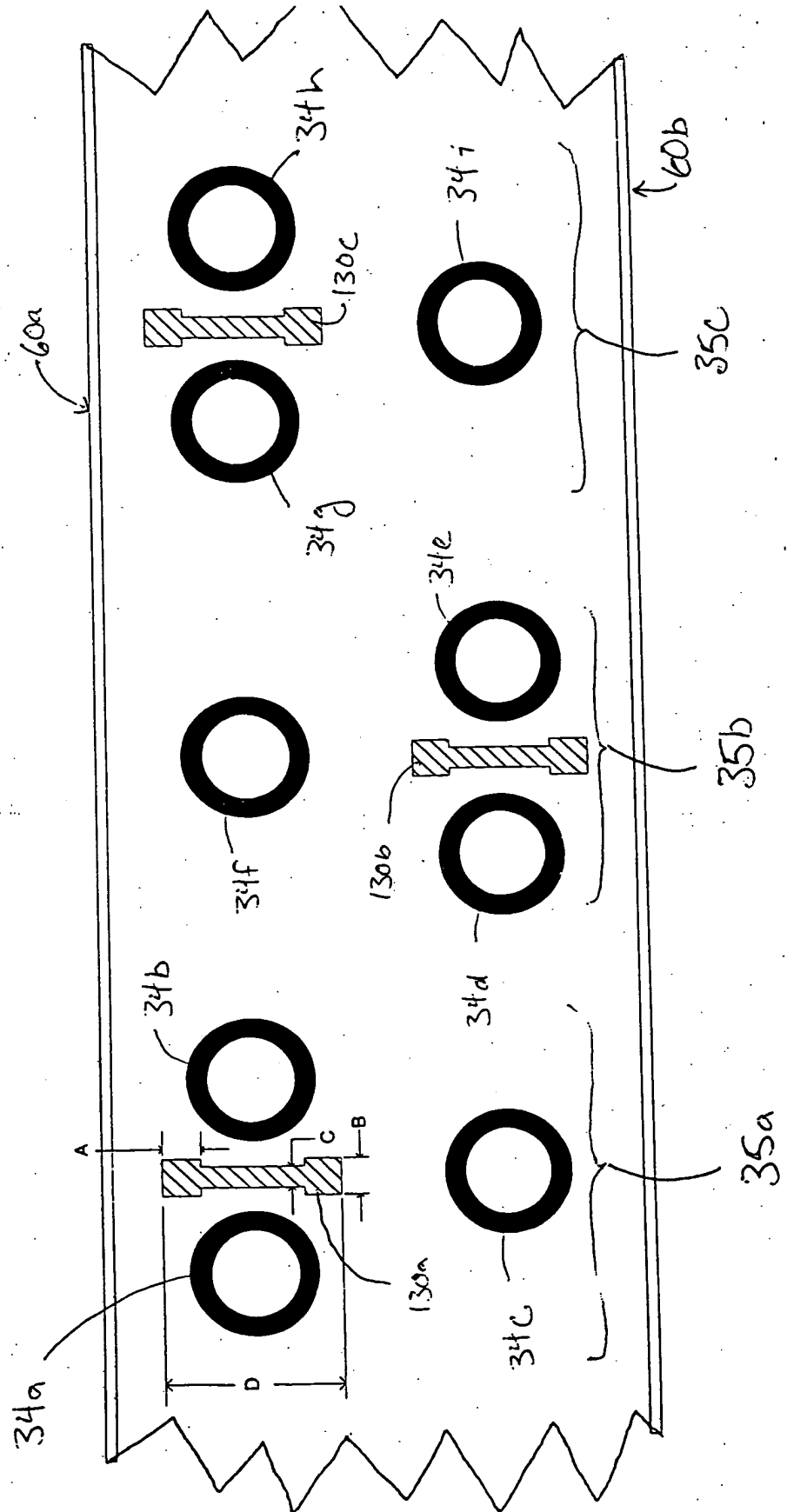


FIG. 9

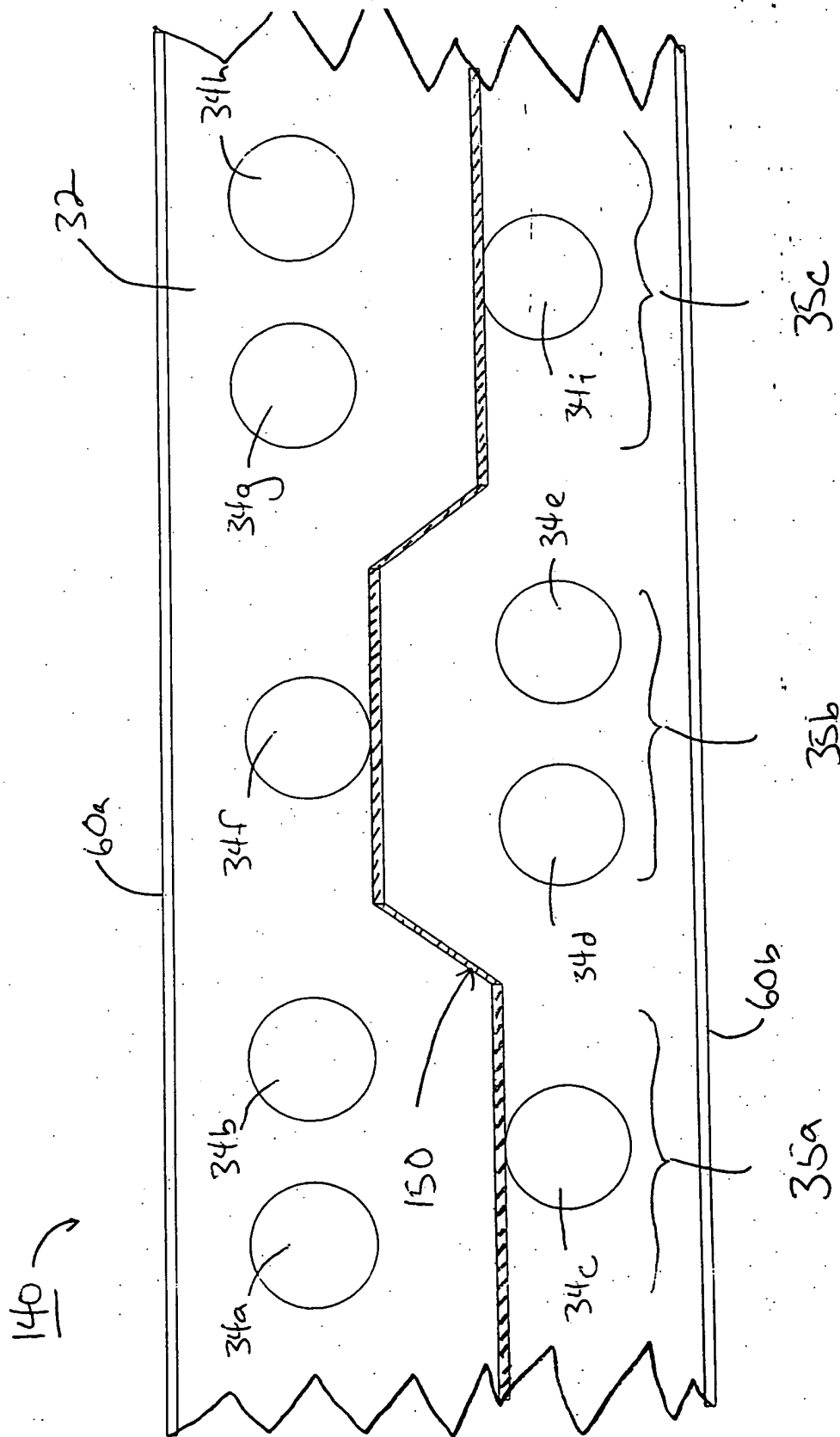


FIG. 10

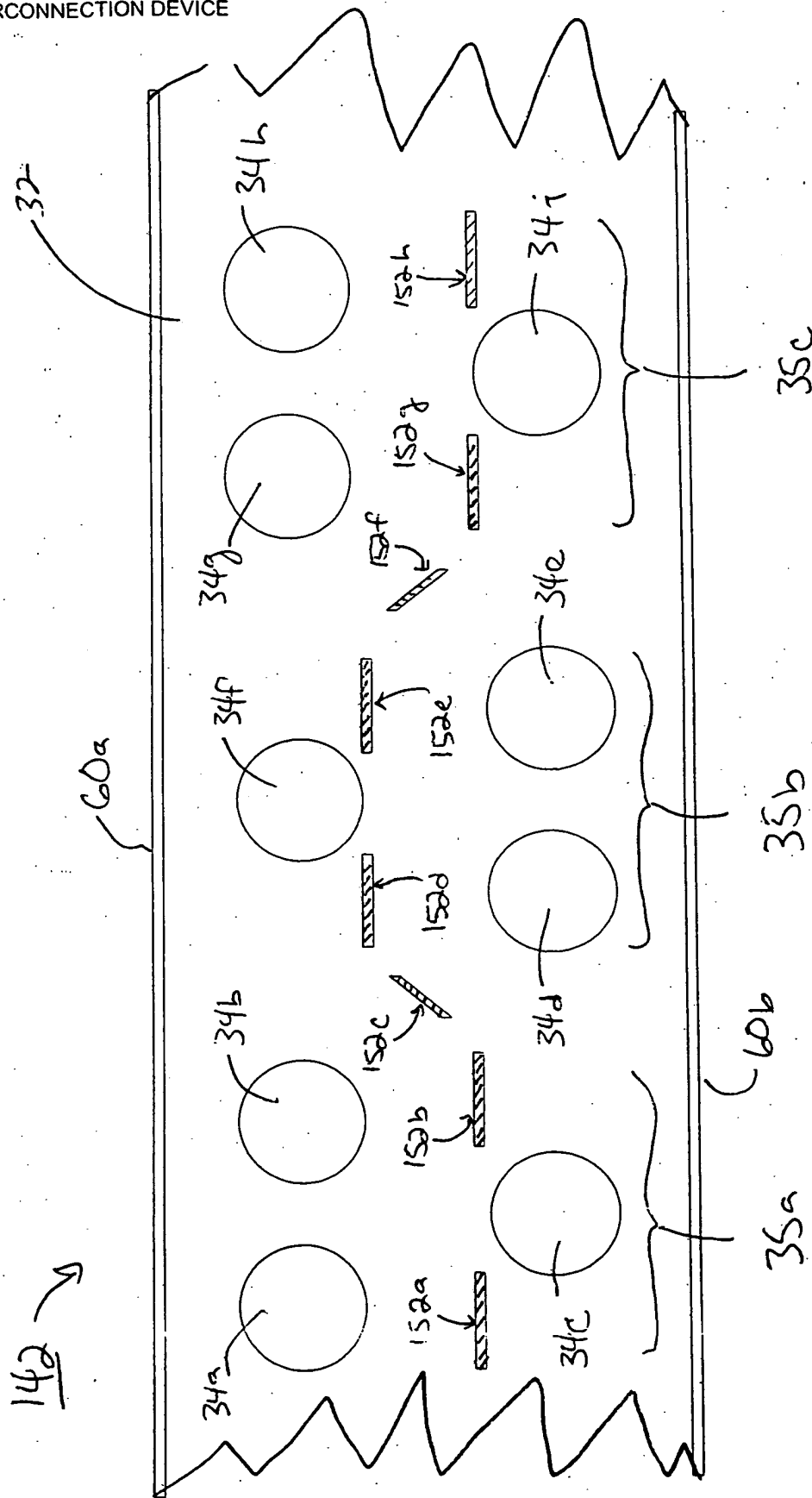


FIG. 11

